



Attorney's Docket No. 4640.P006

PATENT

2133  
#7  
SP  
7-22-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Yervant Zorian

Examiner: Unassigned

Application No.: 10/083,241

Art Unit: 2133

Filed: February 25, 2002

For: APPARATUS AND METHOD TO  
GENERATE A REPAIR  
SIGNATURE

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Technology Center 2100

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

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Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on July 11, 2003.

(Date of Deposit)

Cathy Bachmann

(Typed or printed name of person mailing correspondence)

Cathy Bachmann

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Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

    X     37 C.F.R. §1.97(b).

         37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

         A statement pursuant to 37 C.F.R. §1.97(e) or

         A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

         37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:

- (1) A statement pursuant to 37 C.F.R. §1.97(e); and
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 7-11, 2003



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Substituted Form 1449/PTO			<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>			Application Number	10/083,241
			Filing Date	February 25, 2002
			First Named Inventor:	Yervant Zorian
			Art Unit	2133
			Examiner Name	Unassigned
Sheet	of	2	Attorney Docket Number	04640.P006

### U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)				
		US-	6,237,123 B1	05/22/2001	Kim, et al.	
		US-	6,205,564 B1	03/20/2001	Kim, et al.	
		US-	6,085,334	07/04/2000	Giles, et al.	
		US-	6,317,846 B1	11/13/2001	Higgins, et al.	
		US-	5,570,374	10/29/1996	Yau, et al.	
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### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup>	Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner of Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Substitute Form 1449/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

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**Complete if Known**

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First Named Inventor:	Yervant Zorian
Art Unit	2133
Examiner Name	Unassigned
Attorney Docket Number	04640.P006

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		Yervant Zorian: "System-Chip Test Strategies" (Tutorial): DAC 1998: 752-757.	
		Ilyoung Kim, Yervant Zorian, Goh Komoriya, Hai Pham, Frank P. Higgins, Jim L. Lewandowski: "Built-in Self-Repair for Embedded High Density SRAM." ITC 1998: 1112-1119.	
		Yervant Zorian, Erik Jan Marinissen, Sujit Dey: "Testing Embedded-Core Based System Chips." ITC 1998: pages 1-14.	
		Yervant Zorian, Erik Jan Marinissen, "System Chip Test: How Will It Impact Your Design?" pages 1-6, DAC, 2000.	
		Monica Lobetti Bodoni, Alfredo Benso, Silvia Chiusano, Stefano DiCarlo, Giorgio DiNatale, Paolo Prinetto, "An Effective Distributed BIST Architecture for RAMs," pages 1-6, IEEE European Test Workshop, 2000.	
		Chauchin Su, Shih-Ching Hsiao, Hau-Zen Zhau, Chung-Len Lee, "A Computer Aided Engineering System for Memory BIST," pages 1-4, DAC, 2001.	

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